1	1.	A microcontroller chip, comprising:		
2		a processor for executing program instructions;		
3		an array of configurable circuit blocks configured to perform a circuit		
4	function, such circuit blocks configured to produce an analog output signal; and			
5		an on-chip analog amplifier having an input receiving the analog output		
6	signal and producing an amplified output signal suitable for driving a loudspeaker			
7	external to the microcontroller chip.			
8				
9	2.	The apparatus according to claim 1, wherein the microcontroller chip has		
10	four corners, and wherein the on-chip analog amplifier is situated adjacent on			
11	the four corners.			
12				
13 <u>0</u>	3.	The apparatus according to claim 1, further comprising a switchable current		
14 ⁰ 15.0	source for selectively providing an increase in bias current to the on-chip analog			
15]	amplifier under control of the processor.			
16				
17 <u>.</u> 9	4.	The apparatus according to claim 1, wherein the on-chip analog amplifier		
18]	comprises a tristatable CMOS analog amplifier.			
19 ^[]				
20	5 .	The apparatus according to claim 4, wherein the CMOS analog amplifier		
21 =	comprises a tristate CMOS analog amplifier.			
22				
23	6.	The apparatus according to claim 5, wherein a tristate mode of the tristate		
24	CMOS	analog amplifier is selectably controlled by the processor.		
25				
26	7.	The apparatus according to claim 1, wherein the on-chip analog amplifier		
27	produces an amplified output signal suitable for driving a 32 ohm loudspeaker			
28	external to the microcontroller chip.			
29				

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- 8. The apparatus according to claim 1, wherein the array of configurable circuit blocks comprise an array of analog circuit blocks and an array of digital circuit blocks.
- 9. The apparatus according to claim 8, further comprising a wirebond pad, and wherein the amplified output signal is directed to an external loudspeaker via the wirebond pad.
- 10. The apparatus according to claim 9, further comprising switching means coupled to the wirebonding pad that selectively disables a connection between the configurable circuit blocks and the wirebond pad under processor control.

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11. A microcontroller	chip,	comprising:
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a processor for executing program instructions;

an array of configurable digital circuit blocks configured to perform a digital circuit function;

an array of configurable analog circuit blocks configured to perform an analog circuit function, such analog circuit blocks configured to produce an analog output signal; and

an on-chip CMOS analog amplifier having an input receiving the analog output signal and producing an amplified output signal suitable for driving a loudspeaker external to the microcontroller chip.

- 12. The apparatus according to claim 11, wherein the microcontroller chip has four corners, and wherein the on-chip analog amplifier is situated adjacent one of the four corners.
- 13. The apparatus according to claim 11, further comprising a switchable current source for selectively providing an increase in bias current to the on-chip analog amplifier under control of the processor.
- 14. The apparatus according to claim 11, wherein the on-chip analog amplifier comprises a CMOS analog amplifier.
- 15. The apparatus according to claim 14, wherein a tristate mode of the tristate CMOS analog amplifier is selectably controlled by the processor.
- 16. The apparatus according to claim 11, wherein the on-chip analog amplifier produces an amplified output signal suitable for driving a 32 ohm loudspeaker external to the microcontroller chip.

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17. The apparatus according to claim 11, further comprising a wirebond pad, and wherein the amplified output signal is directed to an external loudspeaker via the wirebond pad; and further comprising switching means coupled to the wirebonding pad that selectively disables a connection between the configurable analog and digital circuit blocks and the wirebond pad under processor control.

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18. A microcontroller chip, comprising:

a processor for executing program instructions;

an array of configurable digital circuit blocks configured to perform a digital circuit function;

an array of configurable analog circuit blocks configured to perform an analog circuit function, such analog circuit blocks configured to produce an analog output signal;

an on-chip tristateable CMOS analog amplifier having an input receiving the analog output signal and producing an amplified output signal suitable for driving a loudspeaker external to the microcontroller chip, wherein a tristate mode of the tristate CMOS analog amplifier is selectably controlled by the processor;

a switchable current source for selectively providing an increase in bias current to the on-chip analog amplifier under control of the processor;

wherein the microcontroller chip has four corners, and wherein the on-chip analog amplifier is situated adjacent one of the four corners.

- 19. The apparatus according to claim 18, further comprising a wirebond pad, and wherein the amplified output signal is directed to an external loudspeaker via the wirebond pad.
- 20. The apparatus according to claim 19, further comprising switching means coupled to the wirebonding pad that selectively disables a connection between the configurable circuit blocks and the wirebond pad under processor control.